IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Qiang Luo

Examiner:

Not Yet Assigned

erial No.:

10/688,657

Group Art Unit:

2612

Filed:

10/17/2003

Docket:

50019.0259US01

Confirmation

1222

Notice of Allow.

Not Yet Assigned

No.:

Date:

Title:

CHARGE DIFFUSION CROSSTALK REDUCTION FOR IMAGE SENSORS

**CERTIFICATE UNDER 37 CFR 1.8:** 

I hereby certify that this correspondence is being deposited with the United States Postal Service as sufficient postage, in an envelope addressed to: , Commissioner for Patents P.O. Fox 1450; Alex on May 11, 2004.

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 PATENT TRADEMARK OFFICE

Sir:

We are transmitting herewith the attached:

☐ Transmittal Sheet in duplicate containing Certificate of Mailing

☐ Information Disclosure Statement, Form 1449, 4 Reference(s)

Return postcard

Please consider this a PETITION FOR EXTENSION OF TIME for a sufficient number of months to enter these papers or any future reply, if appropriate. Please charge any additional fees or credit everpayment to Deposit Account No. 13-2725. A duplicate of this sheet is enclosed.

MERCHANT & GOULD P.C. P.O. Box 2903, Minneapolis, MN 55402-0903 612.332.5300

Name: Mark R. Hennings

Reg. No.: 48,982

MRH

MM 1 4 2004 SEE 10/688,657

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Applicant:

Qiang Luo

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, with sufficient postage, in an envelope addressed to: Commissioner for Patents, B.O. Box 1450, Alexandria A 22313 133 on May 11, 2004

Name: Kayla R Butcher

## **INFORMATION DISCLOSURE STATEMENT (37 C.F.R. § 1.97(b))**

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

With regard to the above-identified application, the items of information listed on the enclosed Form 1449 are brought to the attention of the Examiner.

This statement should be considered because it is submitted before the mailing date of a first Office Action on-the-merits. Accordingly, no fee is due for consideration of the items listed on the enclosed Form 1449.

In accordance with 37 C.F.R. §1.98(a)(2), a copy of each document or other information listed on the enclosed Form 1449 is provided.

No representation is made that a reference is "prior art" within the meaning of 35 U.S.C. §§ 102 and 103 and Applicants reserve the right, pursuant to 37 C.F.R. § 1.131 or otherwise, to establish that the reference(s) are not "prior art." Moreover, Applicants do not represent that a reference has been thoroughly reviewed or that any relevance of any portion of a reference is intended.

Consideration of the items listed is respectfully requested. Pursuant to the provisions of M.P.E.P. 609, it is requested that the Examiner return a copy of the attached Form 1449, marked as being considered and initialed by the Examiner, to the undersigned with the next official communication.

Please charge any additional fees or credit any overpayment to Deposit Account No. 13-2725.

23552
PATENT TRADEMARK OFFICE

Date: May 11, 2004

Respectfully submitted,

MERCHANT & GOULD P.C.

P.O. Box 2903

Minneapolis, MN 55402-0903

206.342.6200

Name: Mark R. Hennings

Reg. No.: 48,982

Date Mailed: May 11, 2004

Sheet 1 of 1

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FORM 1449*	( MM	
	INFORMATION DISCLOSURE STATEME	NT
	TRADEMIN	
	IN AN APPLICATION	

(Use several sheets if necessary)

 Docket Number:
 Application Number:

 50019.0259US01
 10/688,657

Applicant: Qiang Luo

Filing Date: 10/17/2003 Group Art Unit: 2612

		U.S	S. PATENT DOCUMEN	NTS			
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	Orly Yadid	-Pecht; Geometri	ical Modulation Transfer April 2000; pgs. 859-86	Function for Di	-	Area Shapes	; Optical
		Igor Shcherback and Orly Yadid-Pecht; CMOS APS MTF Modeling: IEEE Transactions on Electron Devices, Vol 48, No. 12, December 2001; pgs. 2710-2715					
	C. Marques Crosstalk d	C. Marques, P. Magnan; Experimental Characterization and Simulation of Quantum Efficiency and Optical Crosstalk of CMOS Photodiode APS; Proceedings of SPIE, Vol. 4669, January 21-23, 2002; pgs. 107-116					
	Image Sens	ors and Deblurri	nigan, and R. I. Hornsey ing Operations; IEEE W nau, Germany, 2003	; Empirical Char orkshop on Char	racterization of Lat	eral Crosstal as and Advan	k for CMOS ced Image

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PATENT TRADEMARK OFFICE

EXAMINER	DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form for next communication to the Applicant.